



NEWS RELEASE

SiSoft™ Collaborates with Tektronix to Bridge the Gap Between Simulation and Measurement

SiSoft Helps Tektronix Ensure Seamless Support for IBIS-AMI in New SDLA Visualizer

Santa Clara, CA – January 29, 2013 - Signal Integrity Software, Inc. (SiSoft™), today announced that it is collaborating with Tektronix to bring full support for silicon-specific IP models developed using the IBIS-AMI modeling standard to the [SDLA Visualizer software package](#) from Tektronix. Engineers from both companies are working together to validate the IBIS-AMI implementation in Tek's new SDLA Visualizer framework using real-world data from both simulation and measurement. The joint effort by the two companies will ensure that lab measurements can be post-processed to accurately predict link behavior at test points that are physically inaccessible, and ensure that SiSoft simulation tools provide accurate post-route validation of full system-level designs before PCB fab-out. The combination of simulation and measurement provides a closed-loop design process that improves design predictability and ensures projects meet tight design schedules.

"We chose to work with SiSoft because of their proven leadership in IBIS-AMI," said Brian Reich, general manager, Performance Oscilloscopes Tektronix. "SiSoft has deep experience working with different semiconductor vendors and modeling styles. SiSoft's Certified IBIS-AMI model library allows Tektronix to leverage years of effort working with many vendors and developing correlated models."

SiSoft has more experience in [IBIS-AMI](#) than anyone, having developed and correlated IBIS-AMI models since 2007. SiSoft works with dozens of semiconductor vendors through its IBIS-AMI model certification program to ensure that IBIS-AMI models produce correct simulation results in SiSoft's [Quantum Channel Designer®](#) (QCD) software. [QCD Certified design kits](#) are complete, ready-to-run simulation setups that ensure models are simulated as the IP developer intended, including accurate package models and the effects of jitter and noise. Design kits save designers time and eliminate errors since determining operating margins is as simple as importing their channel model and rerunning analysis. Certification assures designers that models are ready for use in system-level pre- and post-route simulations that accurately predict how real hardware will perform. Certification also ensures that models are interoperable with other vendor's IBIS-AMI models, producing correct results even when two sets of models have differing setup requirements.

"Simulation and measurement must complement each other to ensure first-pass design success," noted Barry Katz, SiSoft's President and CTO. "IBIS-AMI is still an evolving standard, and IBIS-AMI model accuracy is critical. Working with Tektronix on their SDLA Visualizer framework allows us to ensure that the measurement and simulation worlds come together to provide the closed-loop process essential to meeting the design challenges of 28 Gb/s and beyond."

Both SiSoft and Tektronix will be demonstrating the integration between Quantum Channel Designer and SDLA Visualizer in their booths at DesignCon this week.

About SiSoft

SiSoft collaborates with customers and their suppliers to develop innovative solutions to the world's toughest high-speed design problems. SiSoft accelerates design cycles through a combination of award-winning EDA simulation software, methodology training and consulting services. Quantum Channel Designer (QCD) is the Industry's Premier Channel Simulator for the design and analysis of multi-Gigabit serial links and a DesignVision Award Winner. Quantum-SI™ (QSI) is the leading solution for integrated signal integrity, timing and crosstalk analysis of high-speed parallel interfaces. SiSoft's products automate comprehensive pre- and post-route analysis of high speed interfaces, detailing a design's operating voltage and timing margins. More information on SiSoft can be found at www.sisoft.com.

About Tektronix

For more than sixty-five years, engineers have turned to Tektronix for test, measurement and monitoring solutions to solve design challenges, improve productivity and dramatically reduce time to market. Tektronix is a leading supplier of test equipment for engineers focused on electronic design, manufacturing, and advanced technology development. Headquartered in Beaverton, Oregon, Tektronix serves customers worldwide and offers award-winning service and support. Stay on the leading edge at www.tektronix.com.

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